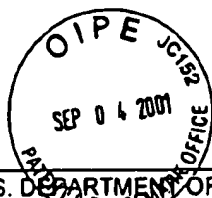


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Sheet 1 of 2

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				ATTY. DOCKET NO.		SERIAL NO.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				11157-23		09/854,905	
(Use several sheets if necessary)				APPLICANT			
				BRIAN MOORE			
				FILING DATE		GROUP	
				May 15, 2001		2858	
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPLICABLE	
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	5,619,463	04/08/97	Malhi	365	201	08/24/95	
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	5,811,983	09/22/98	Lundberg	324	763	09/03/96	
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0911640	04/28/99	Europe	G 01 R 29	027	Not needed
99/16107	04/01/99	PCT	H 01 L	—	Not needed
99/32893	07/01/99	PCT	G 01 R 31	60	Not needed
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- Semiconductor Material and Device Characterization - by Dieter K. Schroder - 1990 - pages 1-40
- CMOS Digital Integrated Circuits - by Sung-Mo (Steve) Kang & Yusuf Leblebici - 1999 - pages 220-242, Chaps 6, 9, 11, 15
- A Self-Tuning Digital Telemetry IC for Use in a Microprocessor-Based Implantable Instrument - IEEE Journal of Solid-State Circuits - Vol. 27, No 12 (Dec. 1992) - by Kenneth W. Fernald, John J. Paulos, Blaine A. Stackhouse & Robert A. Heaton - Pages 1826-1832
- Testing the Monster Chip - IEEE Spectrum, (July 1999) - by Yervant Zorian - Pages 54-60

EXAMINER: Jimmy Nguyen DATE CONSIDERED 8/26/04

*EXAMINER: Initial if citation not considered, whether or not citation is in conformance with MPEP 609; draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	09/854,905
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Filing Date	May 15, 2001
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First Named Inventor	MOORE, Brian
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Group Art Unit	2829
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Examiner Name	N/A
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Attorney Docket Number	11157-23
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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

[illegible]

**Examiner
Signature**

Jimmy Nguyen

Date
Considered

6/4/03

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